

PRODUCT: OMNIWALL® DISPLAY PROCESSOR**TITLE: VERSION 4.4.3.0 RELEASE NOTES**

DATE: 20 JANUARY 2015

NEW FEATURES

The Version 4.4.3.0 feature set is unchanged from Version 4.4.2.0.

NEW COMMANDS

Version 4.4.3.0 adds a new command, **tmdspower**, which powers an HDBaseT output on or off. The command syntax is as follows:

```
ccl output <output#> tmdspower <channel#>,<state>
```

Where **channel#** is the port number on the card (0 or 1), and **state** is the power state (0 = off; 1 = on).

For example, to power off Port 0 on an HDBaseT output card in Slot 3, use the command:

```
ccl output 5 tmdspower 0,0
```

To restore power to Port 0 on an HDBaseT output card in Slot 3, use the command:

```
ccl output 5 tmdspower 0,1
```

RESOLVED ISSUES

This release is a maintenance release to address the following issues.

HDBaseT CARD

- After disconnecting and re-connecting a display, HDCP protected content is restored, but sometimes video that is displayed is clear, un-protected video, the **Video Active** LED on the HDBaseT receiver is turned off, and HDCP has been turned off on the HDBaseT card.
- When routing a null input signal to either port on the HDBaseT card then re-routing a valid HDCP protected content signal, sometimes the HDBaseT output card gets stuck in an infinite failure loop.
- When an input signal is received on either channel, an excessive blanking/un-blanking condition exists on the HDBaseT and Audio output cards. Blanking and un-blanking are now done only when required.
- When a video signal is switched to either output channel on an HDBaseT output card, sometimes, video is displayed on the display but HDCP has been turned off even though the signal is content protected.

- When a video signal is switched to either channel, sometimes the HDCP state machine is stuck and remains in this state with no video on the display and the LED on the receiver is off.
- When a video signal is switched to either channel, sometimes, the HDCP state machine indicates authenticated and the LED on the receiver is on solid, but there is no video on the display.
- Sometimes when an HDCP-protected video signal is presented the system indicates that protected content is present (the **Video Active** LED on the transmitter is solidly ON) but HDCP is not active. This condition was seen most often with Apple devices as an HDCP source (iPad playing movies).

SCALER OUTPUT CARD

- Due to a discrepancy between the vertical line counters, video can be seen coming and going randomly at a somewhat low rate (once every eight seconds or so) across all monitors.
- Sometimes the scaler card will enter into an infinite crash-followed-by-restart loop.

DVI INPUT CARD

- When an HDMI-capable input signal carrying HDCP protected content is presented, the input signal will come and go during the HDCP authentication process.
- When processing AVI info frames, it was observed that an “unexpected packet title” is received from the input device. When this occurs, AVI info frames are not propagated to output ports, resulting in missing audio.

KNOWN ISSUES

- When a Dell 2410 monitor is connected to an HDBaseT output card and the monitor enters the power-save/sleep mode, the monitor may not wake up when a valid signal is returned (HDCP or non-HDCP).
- Occasionally, when a monitor is newly connected to an output port, the embedded audio is not presented in the output stream. If this occurs, use the `outen <output number> off` command, followed by the `outen <output number> on` command.

FIRMWARE UPDATE

The firmware update procedure for Version 4.4.3.0 is unchanged from Version 4.4.1.0. Please refer to the *OmniWall Display Processor Firmware Update Instructions* for Version 4.4.1.0 to update from any previous version.

FIRMWARE, FPGA, AND BOOT CODE VERSIONS

The following is a list of current input/output boards and their current firmware, FPGA, and boot code versions.

Board Name	Board Part Number	Firmware Version	FPGA Version	Boot Code Version
Dual Single Link DVI/RGB Input	625-0551-01	10.009	0.2.20	1.13
Dual Single Link DVI Output	625-0552-01	10.009	0.2.20	2.13
Dual Single Link DVI Input	625-0579-02	10.009	1.1.2	1.13
Single Dual Link DVI/RGB Input	625-0563-02	10.009	2.1.16	1.13
Single Dual Link DVI Output	625-0564-02	10.009	3.2.19	2.13
Single Dual Link HDMI Output	625-0564-03	10.009	3.3.19	2.13
Scaler Output	625-0565-01	10.027	7.2.42	3.13
Scaler NIOS	625-0565-01		2.6.156A	
Scaler HDMI Output	625-0565-02	10.027	7.2.42	3.13
Fiber Input	625-0608-01	10.009	8.1.6	4.13
HD-SDI Input	625-0588-01	10.006	6.1.7	4.13
Fiber Output	625-0609-01	10.009	9.1.10	2.13
Audio Output	620-0622-01 Rev. 2	10.013	10.0.13	2.13
HDBaseT Input	620-0623-01	10.007	11.1.3	4.13
HDBaseT Output	620-0624-01	10.010	11.1.3	2.13

Board Name	Board Part Number	CPLD Version	FPGA Version	Boot PROM
OmniWall 32 Backplane	625-0549-01	1.17	1.26	0.4
OmniWall 16 Backplane	625-0561-01	1.17	1.26	0.4