

PRODUCT: OMNIWALL® DISPLAY PROCESSOR**TITLE: VERSION 4.4.2.0 RELEASE NOTES**

DATE: 1 OCTOBER 2014

NEW FEATURES

The Version 4.4.2.0 feature set is unchanged from Version 4.4.1.0.

NEW COMMANDS

The Version 4.4.2.0 command set is unchanged from Version 4.4.1.0.

RESOLVED ISSUES

- HDCP-protected content is restored reliably after the disconnection and reconnection of an HDBase-T cable.
- When a “null” input signal is routed to an HDBase-T output port, followed by a signal with valid HDCP-protected content, the valid HDCP signal is restored.
- DVI Scaler output integrity has been improved when interlaced input signals are received.
- When using the HDBase-T output card, HDCP performance and overall stability has been improved.
- Single-link DVI input card performance has been improved when HDCP-protected content with audio is received, authenticated, and routed to an output.

KNOWN ISSUES

- When a Dell 2410 monitor is connected to an HDBase-T output card and the monitor enters the power-save/sleep mode, the monitor may not wake up when a valid signal is returned (HDCP or non-HDCP).
- Occasionally, when a monitor is newly connected to an output port, the embedded audio is not presented in the output stream. If this occurs, use the **outen <output number> off** command, followed by the **outen <output number> on** command.

FIRMWARE UPDATE

The firmware update procedure for Version 4.4.2.0 is unchanged from Version 4.4.1.0. Please refer to the *OmniWall Display Processor Firmware Update Instructions* for Version 4.4.1.0 to update from any previous version.

Board Name	Board Part Number	Firmware Version	FPGA Version	Boot Code Version
Dual Single Link DVI/RGB Input	625-0551-01	10.006	0.2.20	1.13
Dual Single Link DVI Output	625-0552-01	10.006	0.2.20	2.13
Dual Single Link DVI Input	625-0579-02	10.006	1.1.2	1.13
Single Dual Link DVI/RGB Input	625-0563-02	10.006	2.1.16	1.13
Single Dual Link DVI Output	625-0564-02	10.006	3.2.19	2.13
Single Dual Link HDMI Output	625-0564-03	10.006	3.3.19	2.13
Scaler Output	625-0565-01	10.021	7.2.42	3.13
Scaler NIOS	625-0565-01		2.6.156	
Scaler HDMI Output	625-0565-02	10.021	7.2.42	3.13
Fiber Input	625-0608-01	10.006	8.1.6	4.13
HD-SDI Input	625-0588-01	10.006	6.1.7	4.13
Fiber Output	625-0609-01	10.006	9.1.10	2.13
Audio Output	620-0622-01 Rev. 2	10.009 (was 10.007)	10.0.13	2.13
HDBaseT Input	620-0623-01	10.007	11.1.3	4.13
HDBaseT Output	620-0624-01	10.009 (was 10.007)	11.1.3	2.13

Board Name	Board Part Number	CPLD Version	FPGA Version	Boot PROM
OmniWall 32 Backplane	625-0549-01	1.17	1.26	0.4
OmniWall 16 Backplane	625-0561-01	1.17	1.26	0.4