

**PRODUCT: OMNIWALL® DISPLAY PROCESSOR****TITLE: VERSION 4.4.1.0 RELEASE NOTES**

DATE: 7 AUGUST 2014

**NEW FEATURES**

The Version 4.4.1.0 feature set is unchanged from Version 4.4.0.0.

**NEW COMMANDS**

The Version 4.4.1.0 command set is unchanged from Version 4.4.0.0.

**GENERAL ENHANCEMENTS**

This release updates the input card boot loader/updater as follows:

1. Modifies the DVI Input card BOOT code to improve the accuracy with which the card detects the power source (+5V pin power from an external input source device, or local-power from the system backplane).

**Note:** After the firmware update completes, you must update the boot code with the `ubt all all` command, and when that step is complete, re-enter the `ufw` command. This will update all I/O card types as follows:

- DVI Input cards will update to boot loader version 1.13.
  - Output cards (except Scaler Output cards) will update to boot loader version 2.13 (version number change only, for consistency; no code changes).
  - Scaler Output cards will update to boot loader version 3.13 (version number change only, for consistency; no code changes).
  - HD-SDI, FIBER and HDBaseT Input cards will update to boot loader version 4.13.
2. Minor changes to the system controller:
    - Add `sysinfo` output dump at end of system startup.
    - Add a `Reset completed` message at the end of system startup to more clearly indicate the system is ready for expected switcher services.
    - Upon system startup, card task init pauses for an additional 20 seconds before starting any interaction with the I/O cards, to allow settling time for the cards.

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## RESOLVED ISSUES

- Video output is no longer interrupted when routing with the same source or changing the preset with the same preset number.
- Preset recall using the front panel controls now works as expected.
- Improved handling of certain Scaler Output card error conditions.
- Improved operation during routing transitions involving HDCP-protected sources.

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## KNOWN ISSUES

- Before performing a system reset, it may be necessary to disconnect interlaced sources from the system (or power them down) in order for the reset to complete successfully.
- Scaler Output card may not output correctly when performing dual-link to single-link scaling. To correct this, use the `sysrst` command.

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## FIRMWARE UPDATE

Please refer to the *OmniWall Display Processor Firmware Update Instructions* for Version 4.4.1.0 to update from any previous version.

| <b>Board Name</b>              | <b>Board Part Number</b> | <b>Firmware Version</b> | <b>FPGA Version</b> | <b>Boot Code Version</b> |
|--------------------------------|--------------------------|-------------------------|---------------------|--------------------------|
| Dual Single Link DVI/RGB Input | 625-0551-01              | 10.006                  | 0.2.20              | 1.13                     |
| Dual Single Link DVI Output    | 625-0552-01              | 10.006                  | 0.2.20              | 2.13                     |
| Dual Single Link DVI Input     | 625-0579-02              | 10.006                  | 1.1.2               | 1.13                     |
| Single Dual Link DVI/RGB Input | 625-0563-02              | 10.006                  | 2.1.16              | 1.13                     |
| Single Dual Link DVI Output    | 625-0564-02              | 10.006                  | 3.2.19              | 2.13                     |
| Single Dual Link HDMI Output   | 625-0564-03              | 10.006                  | 3.3.19              | 2.13                     |
| Scaler Output                  | 625-0565-01              | 10.021                  | 7.2.42              | 3.13                     |
| Scaler NIOS                    | 625-0565-01              |                         | 2.6.156             |                          |
| Scaler HDMI Output             | 625-0565-02              | 10.021                  | 7.2.42              | 3.13                     |
| Fiber Input                    | 625-0608-01              | 10.006                  | 8.1.6               | 4.13                     |
| HD-SDI Input                   | 625-0588-01              | 10.006                  | 6.1.7               | 4.13                     |
| Fiber Output                   | 625-0609-01              | 10.006                  | 9.1.10              | 2.13                     |
| Audio Output                   | 620-0622-01<br>Rev. 2    | 10.007                  | 10.0.13             | 2.13                     |
| HDBaseT Input                  | 620-0623-01              | 10.007                  | 11.1.3              | 4.13                     |
| HDBaseT Output                 | 620-0624-01              | 10.007                  | 11.1.3              | 2.13                     |

| <b>Board Name</b>     | <b>Board Part Number</b> | <b>CPLD Version</b> | <b>FPGA Version</b> | <b>Boot PROM</b> |
|-----------------------|--------------------------|---------------------|---------------------|------------------|
| OmniWall 32 Backplane | 625-0549-01              | 1.17                | 1.26                | 0.4              |
| OmniWall 16 Backplane | 625-0561-01              | 1.17                | 1.26                | 0.4              |