



## Linx Matrix Switchers Version 2.3.1 Release Notes

**LINX™ DVI Matrix Switcher models 800, 1600, and LINX CrossXFormat® Matrix Switcher models 900, 1700, and 3300**

**Contact RGB Technical Support for more details**

### **COMPATIBILITY: V 2.1, 2.2,**

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The following release notes apply to Linx 800, 900, 1600, 1700, 3200, and 3300 switchers. A Linx 1600 or 1700 will not respond to commands intended exclusively for a Linx 3200 or 3300, respectively. A Linx 800 or 900 will not respond to commands intended exclusively for a Linx 1600, 3200, 1700 or 3300.

DVI-only models 800, 1600, 3200 will not respond to commands intended solely for CrossXFormat Switchers 900, 1700, 3300.

### **UPDATE NOTES:**

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**A new procedure is required for this upgrade. These instructions are available as a separate document on the RGB Spectrum support web page and on the Linx product CD. Please read through the complete procedure before attempting a firmware upgrade.**

**The firmware upgrade will erase non-factory-default data in the switch. Use the EXPORTSETTINGS command to save EDID and preset information; the procedure is covered in the *Linx User Guide*. After the upgrade is complete, use the IMPORTSETTINGS command to restore the data.**

1. When performing a firmware upgrade using a serial interface, set the baud rate to 9600.
2. A new command has been added to the Linx to enable/ disable HDCP processing on a port-by-port basis. HDCP processing should be disabled on inputs whose source devices are not HDCP compliant. A user selectively enables or disables processing on an input port. The setting is automatically propagated to any output ports the input port is routed to.

Syntax: HDCP <input # | ALL> [ON | OFF]

Example: To enable HDCP for all inputs, enter the command:

```
hdcp all on
```

Example: To enable HDCP on input 14, type:

```
hdcp 14 on
```

HDCP cannot be enabled/disabled through the WCP.

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3. When the EDID mode is set to **Dynamic** with Priority Arbitration set to **Lowest Number** or **Lowest Resolution**, one or the other must be applied to all outputs. DO NOT set some outputs for **Lowest Number** and others for **Lowest Resolution** at the same time.
4. When using priority arbitration **Lowest Resolution**, do not include outputs which have no display connected in a group. Assign those outputs to an unused input. If this is not possible, the Priority Arbitration will include the factory default EDID in its calculations.

Factory default EDID has a preferred timing of 1920x1200@60Hz. In some cases, all other displays in the group may have a higher resolution. If so, set the no-display outputs to Emulated EDID mode and assign a higher resolution EDID as captured from one of the connected displays.

**Linx release 2.3.1 requires the following:**

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- *Backplane FPGA version 1.22 for the 3200/3300 and 1600/1700*
- *Backplane FPGA version 1.23 for the 800/900*
- *IN\_BOOT version 0.22*
- *Single dual-link input card firmware version 0.100*
- *Single dual-link input card FPGA version 2.1.2*
- *Single dual-link output card firmware version 0.100*
- *Single dual-link output card FPGA version 3.1.2*
- *Dual single-link input card firmware version 0.100*
- *Dual single-link input card FPGA version 0.2.20*
- *Dual single-link output card firmware version 0.100*
- *Dual single-link output card FPGA version 0.2.20*
- *DVI-only single link input card version 0.100*
- *DVI-only single link input card FPGA version 1.1.1*
- *Version 0.4 of the boot loader prom*
- *CPLD firmware version 1.17*
- *Dual-link/ single-link input card FPGA version 2.20 (for units shipped before April 2010)*
- *Dual-link/ single-link output card FPGA version 2.20 (for units shipped before April 2010)*